

Abstracts

An Inverted Microstrip Line IC Structure for Ultra High-Speed Applications

S. Yamaguchi, Y. Imai, T. Shibata, T. Otsuji, M. Hirano and E. Sano. "An Inverted Microstrip Line IC Structure for Ultra High-Speed Applications." 1995 MTT-S International Microwave Symposium Digest 95.3 (1995 Vol. III [MWSYM]): 1643-1646.

An inverted microstrip line IC structure for ultra high-speed applications is proposed. It allows a very low-parasitic impedance module with a flip chip bonding, a small IC size. and performance improvements for high-speed digital ICS.

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